

# Electronics assistant design engineers

(2 interns)

Université de Limoges + Groupe Legrand



**NICOLAS BAUDIN**  
INTERNSHIPS IN FRANCE INITIATIVE

<b>Name of the hosting institution in France</b>	Université de Limoges
<b>Name of the host laboratory / research team</b>	XLim
<b>Address</b>	ENSIL Parc d'Ester Technopole 87069 Limoges
<b>Web site</b>	<a href="https://www.xlim.fr">https://www.xlim.fr</a>
<b>Name of the supervisor</b>	Laurent Billonnet
<b>Function</b>	Professor of Electronics
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<b>Phone number</b>	0033 607615697

## Internship offer

**Topic of the internship (title)** Electronics assistant design engineers

**Proposed dates of the internship** **Start:** 2019-09-02 **End** 2020-01-31

**Scientific and academic objectives of the internship (detailed description of the internship content, work expected from the intern and expected outcomes):**

In the framework of our commercial systems developments, we are looking for two electronics assistant design engineers. Reporting to the Design Department of the Building Systems SBU, you will take on the following assignments:

- Hardware developments (Analog/Digital/Power electronics)
- Production of diagrams and electronic circuit boards,
- Fine tuning
- Development of embedded software on 8- and 32-bit MicroControllers
- Drafting Firmware in C/C++ language
- Setting-up of automated test benches
- Design of automated test platforms
- Use of software programs, e.g. RobotFramework, Jenkins, Ranorex, etc.
- Setting-up of systems qualification means
- Setting-up of systems tests to ensure the systems quality of our offerings
- Participation in the various steps of a project as part of an AGILE team.

<b>Name of industrial partner</b>	Groupe Legrand
<b>Role of the industrial partner in the internship project</b>	Groupe Legrand in Limoges will host the students during their internships and the latter will be supervised by Legrand engineers. Rina Tuioti (Legrand Australia) and Isabelle Desport have planned discussions in the coming weeks to determine the respective roles and commitments on both sides.
<b>Main contact at the French industrial partner</b>	Isabelle Desport
<b>Main contact at the French industrial partner's branch in Australia (if applicable)</b>	Rina Tuioti
<b>Targeted Australian university</b>	Any

## Expected profile of applicant

<b>Level of study</b>	Ideally graduate students preparing a Master's degree
<b>Discipline</b>	An engineering background, ideally preparing a master's degree in electronics, and experience in electronics design
<b>Required qualities, knowledge and skills</b>	Good knowledge of electronics, firmware & software development, and mechatronics